Attorney's

Docket No.: S01022.81033

Express Mail Label No.: EV 292 548 215 US

- 3 -

**IN THE SPECIFICATION** 

Please amend the specification as shown below.

Please replace the paragraphs beginning on page 4, line 2 with the following:

The present invention aims at a method and a circuit for providing a horizontal scanning synchronization signal for a TV set, which are little less sensitive to parasitic pulses.

To achieve this object and other objects, the present invention provides a method for providing a horizontal scan control signal for a TV set from a horizontal synchronization signal contained in a composite video signal, the horizontal synchronization signal containing horizontal synchronization pulses and parasitic pulses, said scan control signal being provided from an oscillating signal generated by an oscillator of a phase-locked loop receiving the horizontal synchronization signal, said oscillating signal having a frequency depending on a driving signal provided from the comparison of the horizontal synchronization signal and of a binary phase signal, in which, at each parasitic pulse among successive parasitic pulses between two synchronization pulses, the driving signal is successively varied in the increasing direction or in the decreasing direction.

Please replace the paragraph beginning on page 4, line 27 with the following:

According to an embodiment of the present invention, the circuit further comprises a comparator for comparing the horizontal synchronization signal and a modified phase signal and providing, according to the comparison, a current of zero amplitude or of constant amplitude and of variable sign; a capacitor run through by conducting the current and providing the driving signal; and a correction circuit providing the comparator with the modified phase signal corresponding to a binary phase signal having a frequency proportional to the frequency of the oscillating signal or corresponding to a binary correction signal, the state of which switches for each parasitic pulse.

Attorney's Express Mail Label No.: EV 292 548 215 US

Docket No.: S01022.81033 - 4 -

Please replace the paragraph beginning on page 6, line 6 with the following:

The principle of the present invention consists of comprises modifying signal PH so that for each parasitic pulse, comparator 22 provides a current pulse  $I_{PLL}$ , having a constant amplitude but the sign of which alternates so that control signal  $S_C$  of oscillator 26 varies generally as little as possible.

Please replace the paragraph beginning on page 7, line 12 with the following:

Filter 53 of circuit 50 comprises two series-assembled connected current sources 60, 61 between a high voltage  $V_R$  and the ground. Current source 60 is controlled by horizontal synchronization signal  $S_{HS}$ . Current source 61 is controlled by the inverse of signal  $S_{HS}$ , designated as  $\overline{S}_{HS}$ . The terminal common to sources 60, 61 is connected to a terminal of a capacitor 62, the other terminal of which is grounded. Voltage  $S_{CC}$  across capacitor 62 is applied to the non-inverting input (+) of a voltage comparator 63. A switch 64 controlled by line gating signal  $S_{LG}$  is assembled in parallel with capacitor 62. The inverting input (-) of comparator 63 receives a reference voltage  $V_{REF}$ .

Please replace the paragraph beginning on page 7, line 30 with the following:

In the present example of embodiment, logic gate 56 comprises at least four inputs. The first input receives signal  $S_{LG}$  inverted by inverter 58. The second input receives signal  $S_{M}$ . The third input receives a phase-locked loop validation signal  $S_{PLLV}$  which is at 1 when phase-locked loop 20 is synchronized in phase. The fourth input receives a standard validation signal  $S_{STAND}$  which is at 1 when the composite video signal CVBS received by the TV set corresponds to a recognized television standard. In particular, signal  $S_{M}$  is modulated in width according to the transmission standard, that is, according to signal  $S_{STAND}$ . Logic gate 56 may receive other signals  $S_{OTHER}$  eonsisting comprising, for example-of, validation signals of specific components of the TV set.

Attorney's

Docket No.: S01022.81033

Express Mail Label No.: EV 292 548 215 US

- 5 -

Please replace the paragraph beginning on page 9, line 15 with the following:

Of course, the present invention is likely to have various alterations, modifications, and improvement improvements which will readily occur to those skilled in the art. In particular, voltage-controlled switch 55 may be formed in any known manner.